

FIG. 1

10/2290-28626860

102290" 28626860

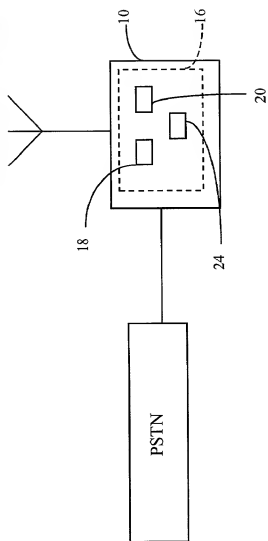


FIG. 2

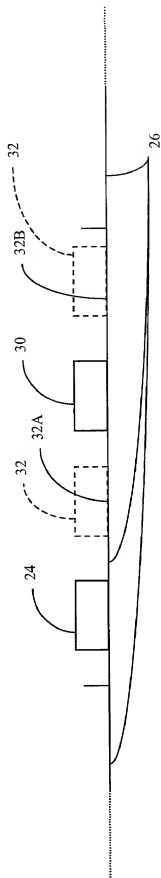


FIG. 3

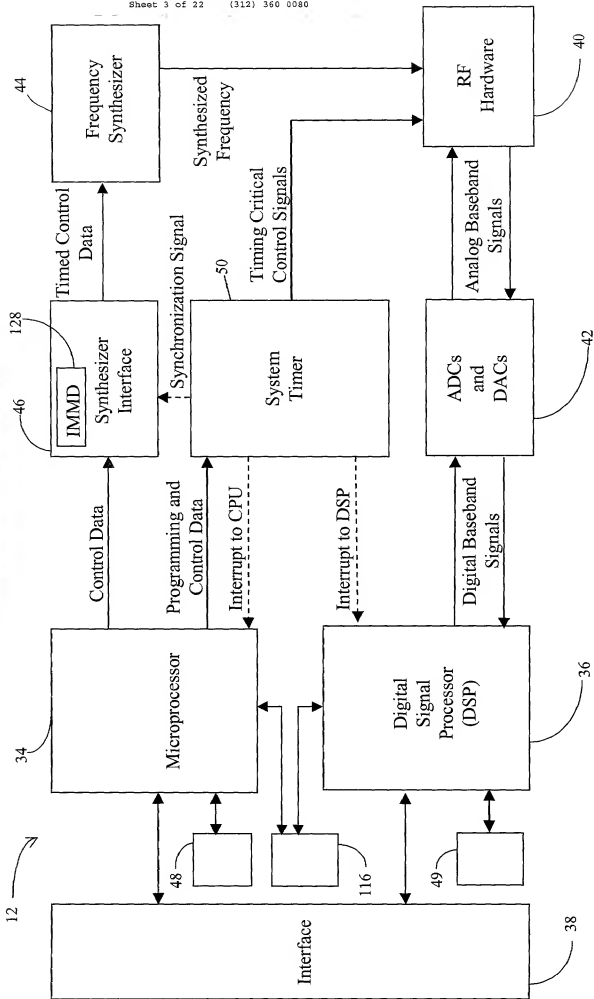


FIG. 4

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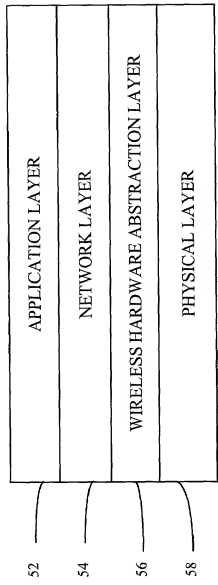


FIG. 5

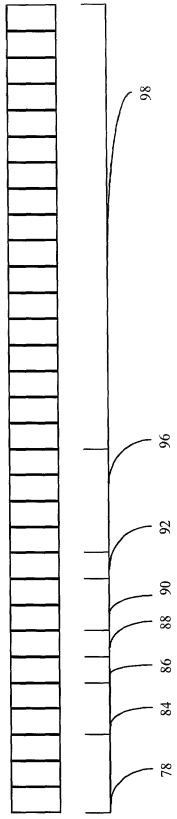


FIG. 7

TO 2290-28626860

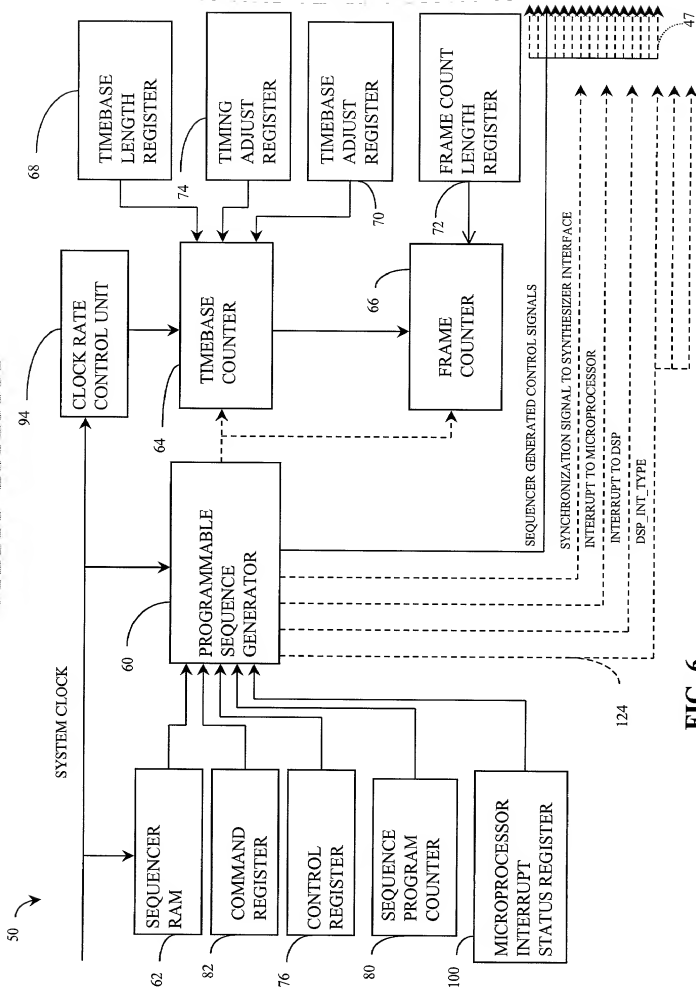


FIG. 6

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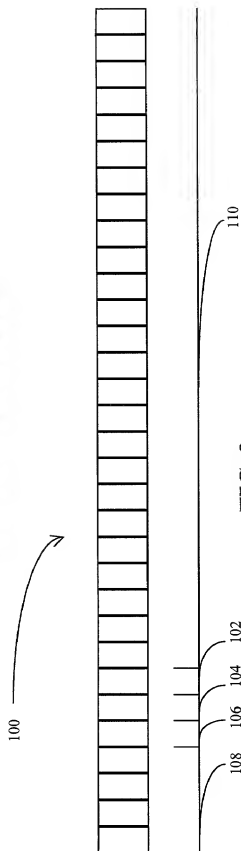


FIG. 8

TYPES OF DSP INTERRUPTS	
146	INVALID
148	FRAME
150	RXWIN
152	TXWIN
154	HANDOFF
156	APC
158	TXWINOFF
157	RESERVED

FIG. 10

TO 2290 28626860

	INSTRUCTION	FORMAT
112	JCMD	JCMD aaaa aaaa
114	JMP	JMP ABCD aaaa aaaa
117	SET	SET m, k, j
118	CLR	CLR m, k, j
120	DSPINT	DSPINT tttt
122	ARMINT	ARMINT tttt
126	SYNSEND	SYNSEND
132	RXENA	RXENA
133	RXDIS	RXDIS
134	TXSTART	TXSTART
136	RTBWAIT	RTBWAIT w
138	FRWAIT	FRWAIT w
140	TBWAIT	TBWAIT w
142	TBADISN	TBADISN
144	NOP	NOP

FIG. 9

102290-48626860

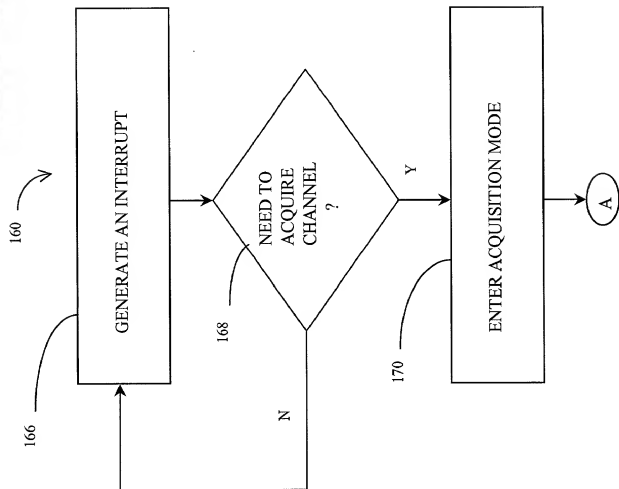


FIG. 12

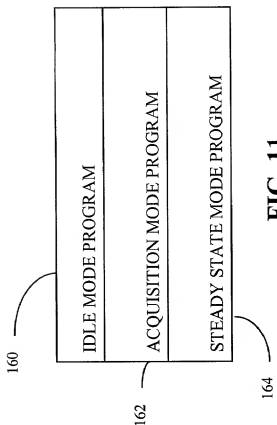


FIG. 11

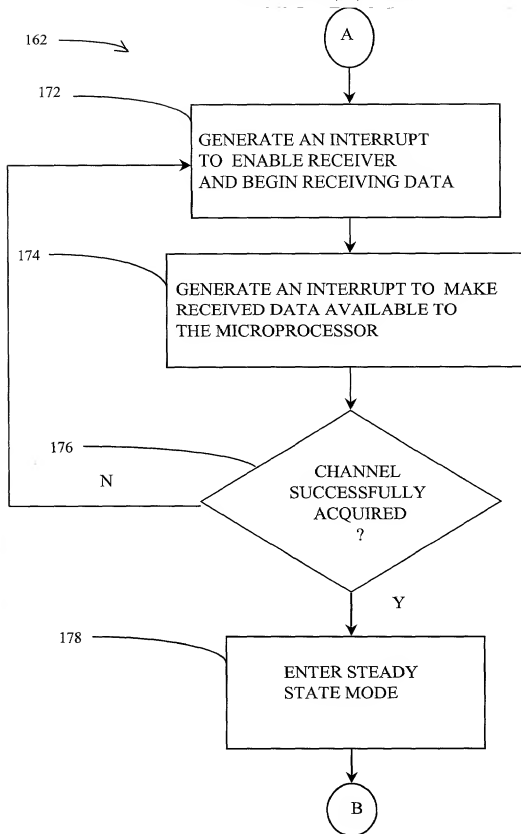


FIG. 13

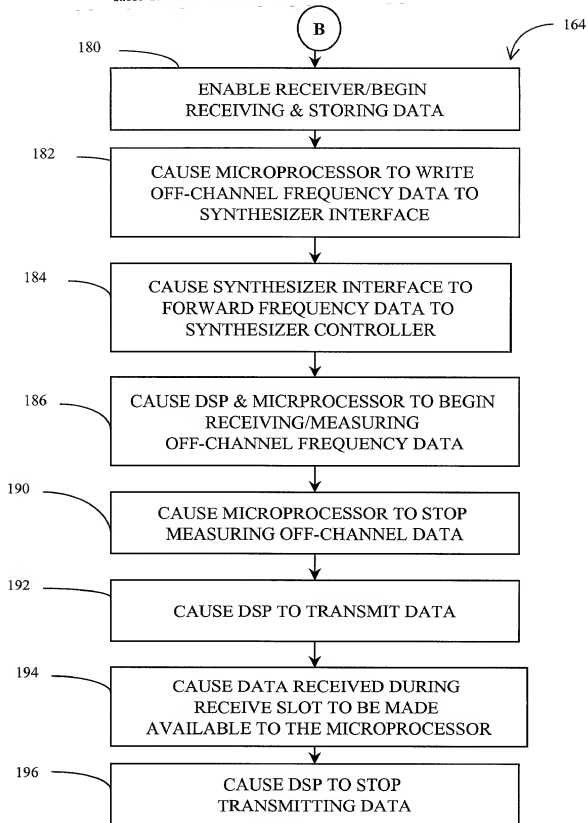


FIG. 14

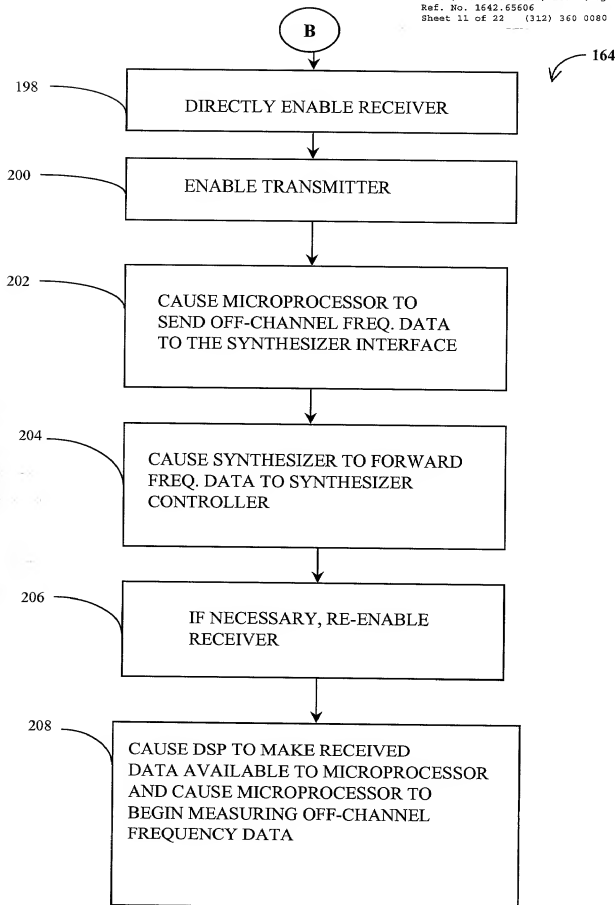


FIG. 15

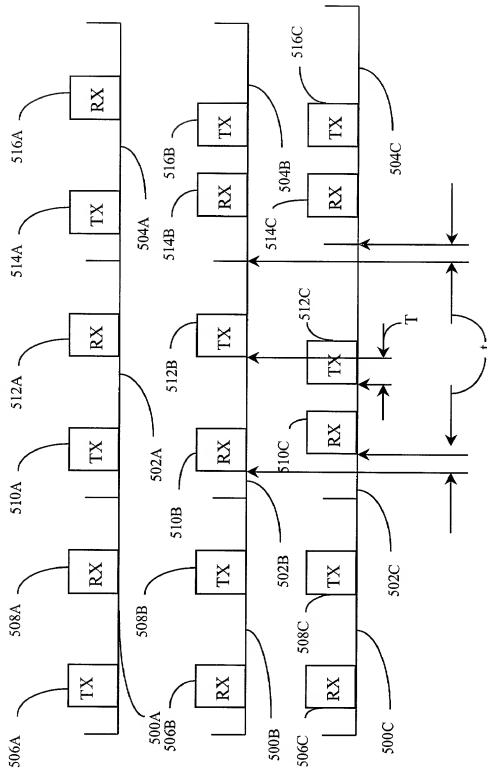


FIG. 16

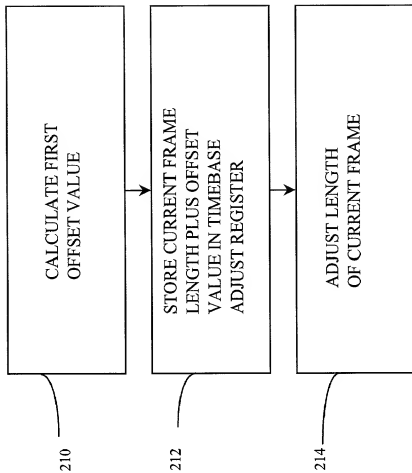


FIG. 17

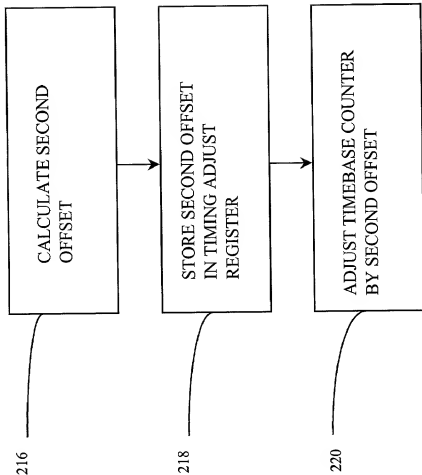


FIG. 18

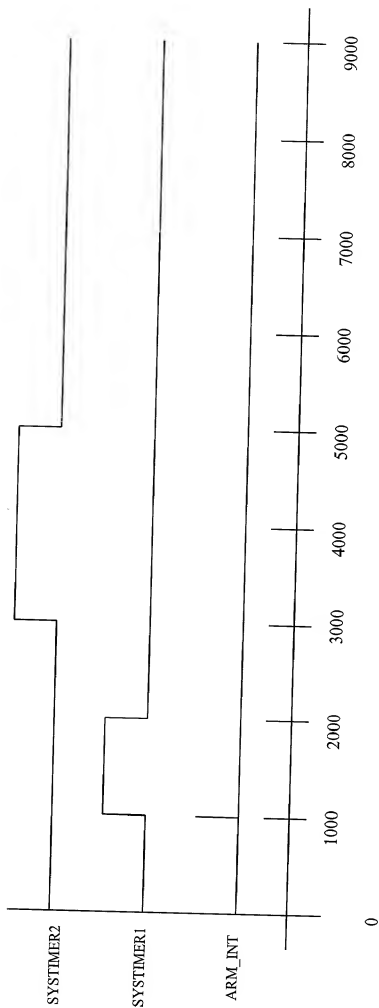


FIG. 19

222

SEQUENCER RAM ADDRESS	INSTRUCTION	
0	TBWAIT 1000	224
1	ARMINT 3	226
2	SET 1,1,1	228
3	TBWAIT 2000	230
4	CLR 1,1,1	232
5	TBWAIT 3000	234
6	SET 2,2,2	236
7	TBWAIT 5000	238
8	CRL 2,2,2	240
9	JCMD 0	242

FIG. 20

244 ↘

SEQUENCER RAM ADDRESS	INSTRUCTION	
0	JMP 1000, 6	246
1	TBWAIT 1000	248
2	ARMINT 3	250
3	SET 1, 1, 1	252
4	TBWAIT 2000	254
5	CLR 1, 1, 1	256
6	JMP 0100, 11	258
7	TBWAIT 3000	260
8	SET 2, 2, 2	262
9	TBWAIT 2000	264
10	CLR 2, 2, 2	266
11	JMP 0010, 16	268
12	TBWAIT 6000	270
13	SET 2, 2, 2	272
14	TBWAIT 6500	274
15	CLR 2, 2, 2	276
16	JMP 0001, 21	278
17	TBWAIT 7000	280
18	SET 2, 2, 2	282
19	TBWAIT 7500	284
20	CLR 2, 2, 2	286
21	JCMD 0	288

FIG. 21

0989987-062701

SEQUENCER RAM ADDRESS	INSTRUCTION	290
0	JCMD 1	292
1	TBWAIT 1000	294
2	ARMINT 3	296
3	SET 1, 1, 1	298
4	RTBWAIT 1000	300
5	JCMD 6	302
6	CLR 1, 1, 1	304
7	JCMD 8	306
8	TBADJSN 0	308
9	RTBWAIT 2000	310
10	SET 2, 2, 2	312
11	RTBWAIT 1000	314
12	JCMD 13	316
13	CLR 2, 2, 2	318
14	TBADJSN 1	320
15	JCMD 0	322

FIG. 22

09892987.062701

FIRST PROGRAM

0	TBWAIT 50
1	JCMD 0

RXSTART PROGRAM

2	RTBWAIT 30
3	SET 1, 1, 1
4	RTBWAIT 20
5	RXENA
6	RTBWAIT 50
7	JCMD 0

RXEND PROGRAM

8	RTBWAIT 50
9	RXDIS
10	RTBWAIT 20
11	CLR 1, 1, 1
12	RTBWAIT 30
13	JCMD 0

TXSTART PROGRAM

14	RTBWAIT 29
15	SET 2, 2, 2
16	RTBWAIT 20
17	TXSTART
18	RTBWAIT 50
19	JCMD 0

FIG. 23A

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TXEND PROGRAM

20	RTBWAIT 70	372
21	CLR 2, 2, 2	374
22	RTBWAIT 30	376
23	JCMD 0	378
		380

REPEAT PROGRAM

24	RTBWAIT 100	382
25	JCMD 0	384
		386

LAST PROGRAM

26	RTBWAIT 50	388
27	JCMD 0	390
		392

ADVANCE PROGRAM

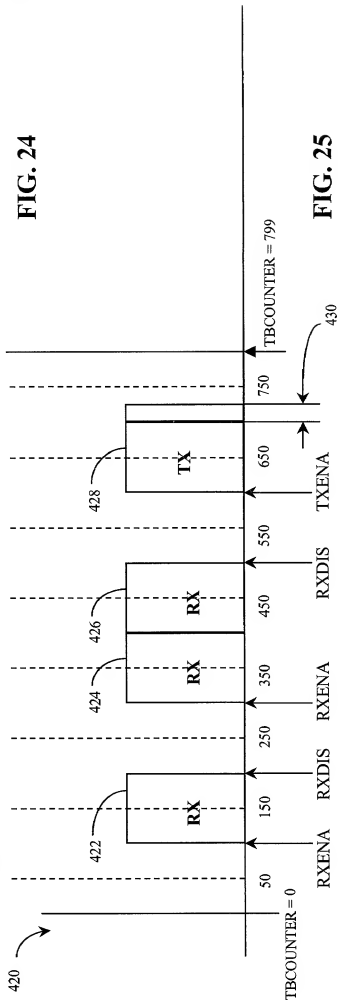
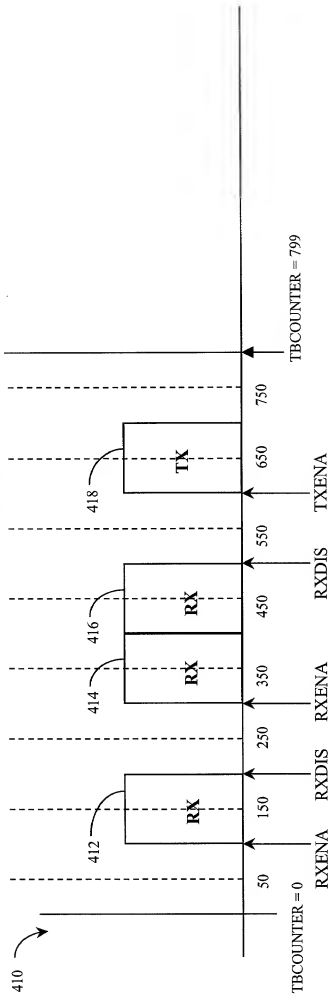
28	TBADJSN 0	394
29	JCMD 0	396
		398

RETARD PROGRAM

30	TBADJSN 1	400
31	JCMD 0	402
		404

FIG. 23B

09892987, 062701



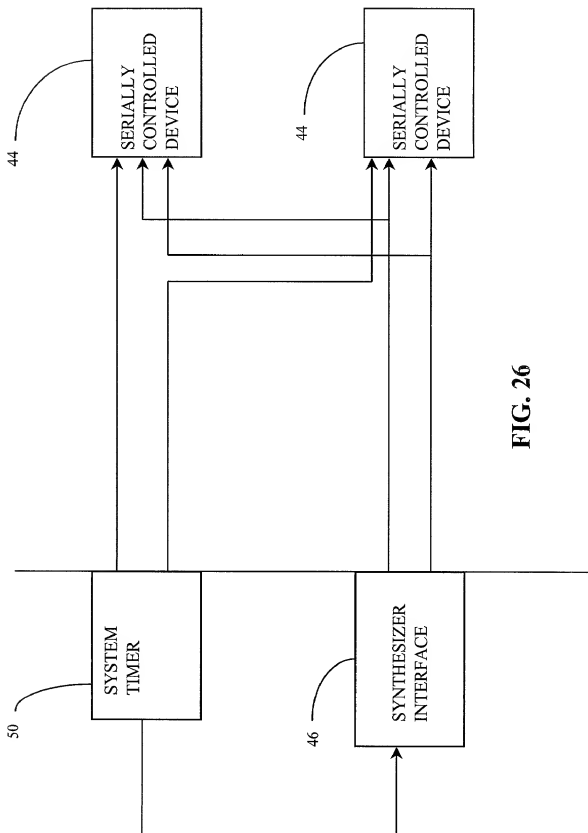


FIG. 26

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